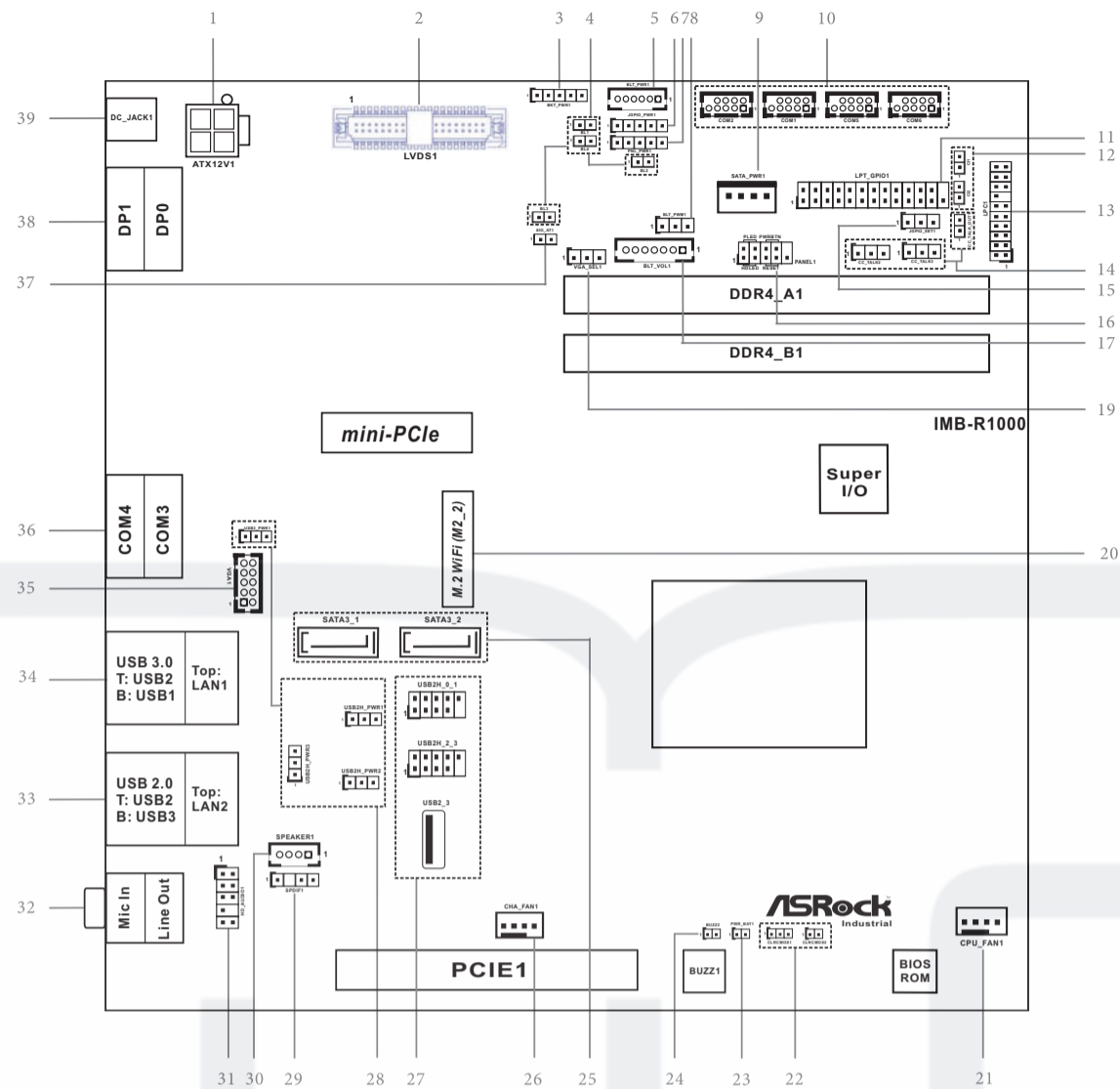
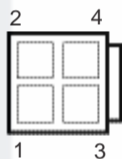


# ASRock Jumpers and Headers Setting Guide

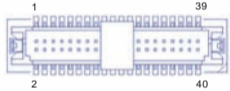
## IMB-R1000



**1 : ATX Power Connector**  
1-2 : GND  
3-4 : DC Input



**2 : LVDS Panel Connector**



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	R_LVDD	17	LVS_A_DATA3#		
2	NA	18	LVS_A_DATA3		
3	+3V	19	LVS_A_DATA2#		
4	NA	20	LVS_A_DATA2		
5	NA	21	LVS_A_DATA1#		
6	LVS_A_DATA0#	22	LVS_A_DATA1		
7	LVS_A_DATA0	23	LVS_B_DATA2#		
8	GND	24	LVS_B_DATA2		
9	LVS_A_DATA1#	25	LVS_B_DATA1#		
10	LVS_A_DATA1	26	LVS_B_DATA1		
11	GND	27	LVS_B_DATA0#		
12	LVS_A_DATA2#	28	LVS_B_DATA0		
13	LVS_A_DATA2	29	LVS_B_DATA0#		
14	LVS_A_DATA3#	30	LVS_B_DATA0		
15	LVS_A_DATA3	31	LVS_B_DATA0		
16	LVS_A_CLK#	32	LVS_B_DATA0		
17	GND	33	LVS_B_CLK#		
18	LVS_A_CLK	34	CON_LBKLT_EN		
19	LVS_A_DATA0#	35	LCD_BLT_VCC		
20	LVS_A_DATA0	36	LCD_BLT_VCC		
21	LVS_A_DATA1#	37	LCD_BLT_VCC		
22	LVS_A_DATA1	38	LCD_BLT_VCC		
23	LVS_B_DATA2#	39	LCD_BLT_VCC		
24	LVS_B_DATA2	40	LCD_BLT_VCC		
25	LVS_B_DATA1#				
26	LVS_B_DATA1				
27	LVS_B_DATA0#				
28	LVS_B_DATA0				
29	LVS_B_DATA0#				
30	LVS_B_DATA0				
31	LVS_B_CLK#				
32	LVS_B_CLK#				
33	CON_LBKLT_EN				
34	CON_LBKLT_EN				
35	LCD_BLT_VCC				
36	LCD_BLT_VCC				
37	LCD_BLT_VCC				
38	LCD_BLT_VCC				
39	LCD_BLT_VCC				
40	LCD_BLT_VCC				

**\* eDP Connector**  
(on the Backside of PCB)



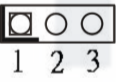
**6 : Digital Input / Output Power Select (JGPIO\_PWR1) (JGPIO\_PWR1)**  
1-2 : +12V  
2-3 : +5V  
3-4 : +5V  
4-5 : GND



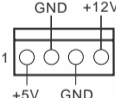
**7 : Panel Power Select (R\_LVDD) (PNL\_PWR1)**  
1-2 : R\_LVDD: +3V  
2-3 : R\_LVDD: +5V  
4-5 : R\_LVDD: +12V



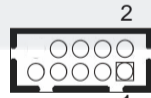
**8 : Backlight Control Level (CON\_LBKLT\_CTL) (BLT\_PWM1)**  
1-2 : From eDP PWM to CON\_LBKLT\_CTL (+3V)  
2-3 : From LVDS PWM to CON\_LBKLT\_CTL (+5V)



**9 : SATA Power Output Connector**



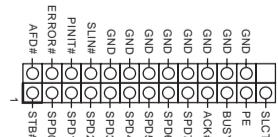
**10 : COM Port Headers (COM1, 2, 5, 6) (RS232)**



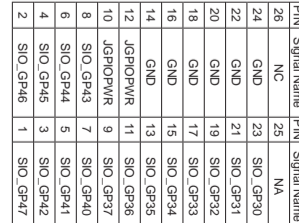
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD		
9	NA	7	RRTS#	5	GND	3	TTXD	1	DDCD#

**11 : Printer Port / GPIO Header (LPT\_GPIO1)**

**Printer Port:**



**GPIO:**

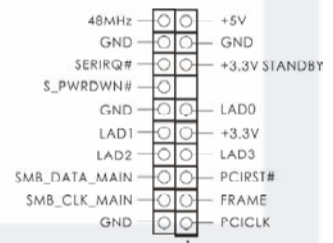


\* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO\_PWR1).

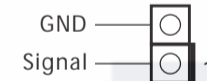
**12 : Chassis Intrusion Headers**  
CI1 :  
Close : Active Case Open  
Open : Normal  
CI2 :  
Close : Normal  
Open : Active Case Open



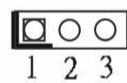
**13 : LPC Header**



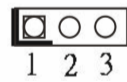
**14 : CC\_TALK\_OUT1**



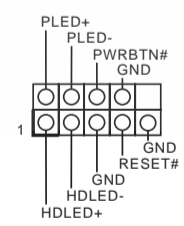
**CC\_TALK2, CC\_TALK3**  
1-2 : CCTALK  
2-3 : COMF



**15 : Digital Input / Output Power Select (JGPIO\_SET1) (JGPIO\_SET1)**  
1-2 : Pull-High  
2-3 : Pull-Low



**16 : System Panel Header**



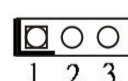
**17 : Backlight & Amp Volume Control (BLT\_VOL1)**



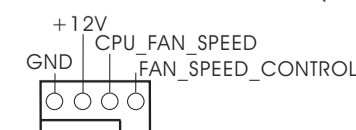
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name		
7	GND	6	GND	5	GPIO_BLT_DW	4	GPIO_BLT_UP	3	PWRDN	2	GPIO_VOL_DW	1	GPIO_VOL_UP

\* To make the backlight level work properly, please make sure that the interval on button clicks is more than one second.

**19 : VGA\_SEL1**  
1-2 : NA  
2-3 : VGA1



**21 : CPU FAN Connector (+12V)**



**4 : BL1 :**

Open : Protect LCD\_BLT\_VCC  
Short : No Protect LCD\_BLT\_VCC

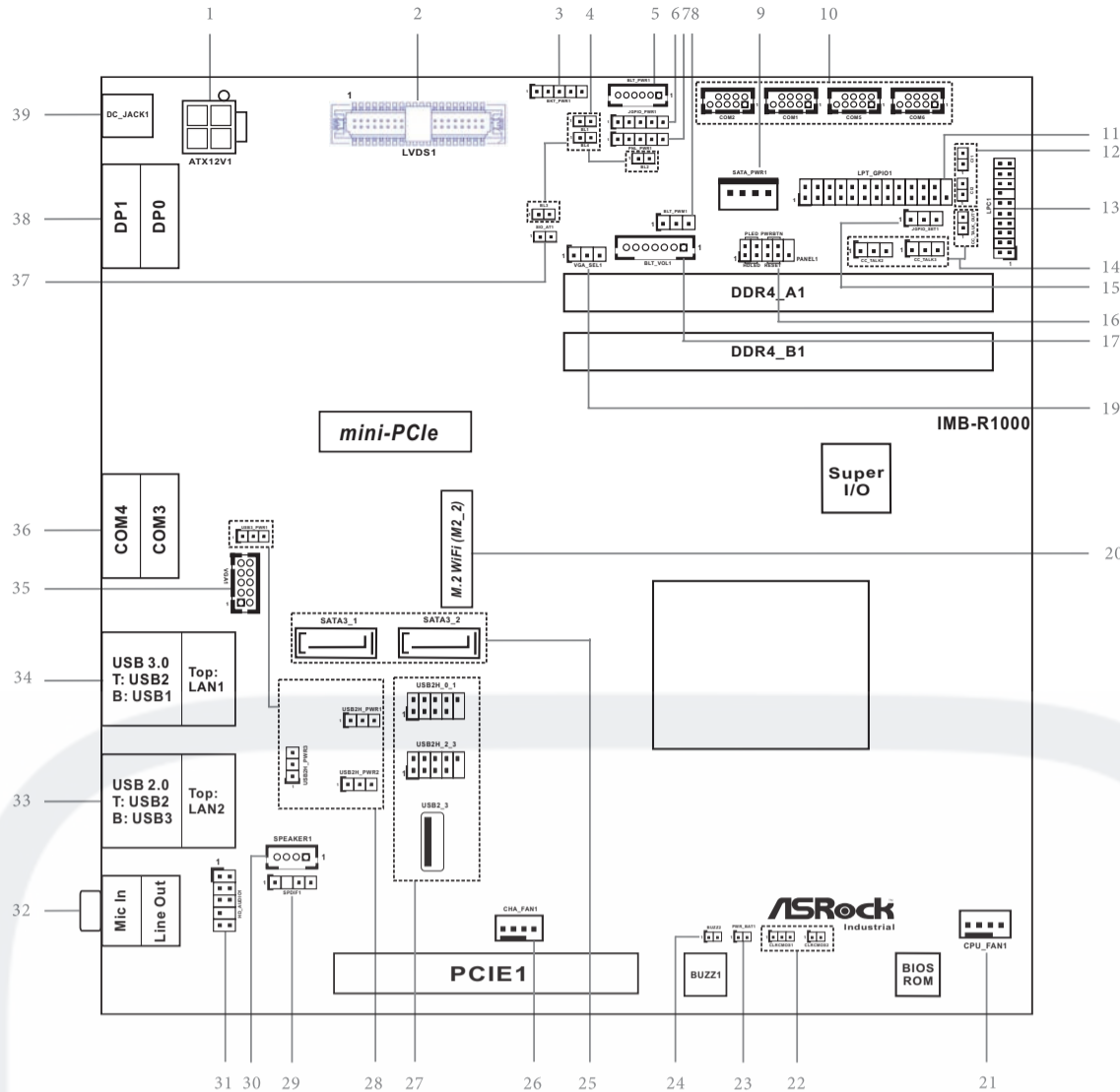
**BL2 :**

Open : Protect R\_LVDD  
Short : No Protect R\_LVDD

**5 : Inverter Power Control Wafer (BLT\_PWR1)**



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name		
6	LCD_BLT_VCC	5	LCD_BLT_VCC	4	CON_LBKLT_EN	3	CON_LBKLT_CTL	2	GND	1	GND



**Back Side :  
Key-M M.2 Socket (M2)**

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	NA	NA	6
7	NA	NA	8
9	GND	SATA_LED	10
11	NA	+3.3V	12
13	NA	+3.3V	14
15	GND	+3.3V	16
17	NA	+3.3V	18
19	NA	NA	20
21	GND	NA	22
23	NA	NA	24
25	NA	NA	26
27	GND	NA	28
29	NA	NA	30
31	NA	NA	32
33	GND	NA	34
35	NA	NA	36
37	NA	NA	38
39	GND	SMB_CLK	40
41	SATA-B+	SMB_DATA	42
43	SATA-B-	NA	44
45	GND	NA	46
47	SATA-A-	NA	48
49	SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

**20 : Key-E M.2 Socket (M2\_2)**

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	NA	NA	10
11	NA	NA	12
13	NA	NA	14
15	NA	NA	16
17	NA	GND	18
19	NA	NA	20
21	NA	NA	22
23	NA	NA	24
33	GND	NA	32
35	PETp	NA	34
37	PETn	NA	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	NA	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE#	54
57	GND	W_DISABLE#	56
59	NA	SMB_DATA	58
61	NA	SMB_CLK	60
63	GND	NA	62
65	NA	NA	64
67	NA	NA	66
69	GND	NA	68
71	NA	NA	70
73	NA	+3.3V	72
75	GND	+3.3V	74

**28 : USB Power Setting Jumpers**  
 USB2H\_PWR1 (For USB2H\_0\_1)  
 USB2H\_PWR2 (For USB2H\_2\_3)  
 USB2H\_PWR3 (For USB2H\_2\_3 on rear I/O)  
 USB3\_PWR1 (For USB3\_1\_2 on rear I/O)

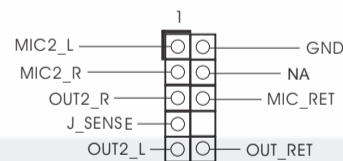
1-2 : +5V  
 2-3 : +5VSB

**29 : SPDIF Header**

**30 : 3W Audio AMP Output Wafer**

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
4	OUTRN	3	OUTRP	2	OUTLP	1	OUTLN

**31 : Front Panel Audio Header**



**32 : Audio Jacks**  
 Green - Line Out  
 Pink - Mic In

**33 : Top : RJ45 LAN Port (LAN2)  
 Bottom : USB 2.0 Ports (USB2H\_2\_3)**

**34 : Top : RJ45 LAN Port (LAN1)  
 Bottom : USB 3.0 Ports (USB3\_1\_2)**

**35 : VGA Header**

PIN	Signal Name	PIN	Signal Name
9	DDC_CLK	10	DDC_DATA
7	HSYNC	8	VSYNC
5	BLUE	6	GND
3	GREEN	4	GND
1	RED	2	GND

**22 : Clear CMOS Headers**

**CLRCMOS1 :**

1-2 : Normal

2-3 : Clear CMOS

**CLRCMOS2 :**

Open : Normal

Short : Auto Clear CMOS (Power Off)

**23 : PWR\_BAT1**

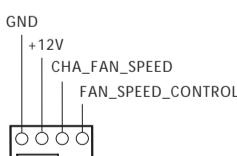
Open : Normal

Short : Charge Battery

**24 : Buzzer (BUZZ2)**

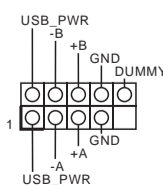
**25 : SATA3 Connectors (SATA3\_1, SATA3\_2)**

**26 : Chassis FAN Connector (+12V)**



**27 : USB 2.0 Headers (USB2H\_0\_1, USB2H\_2\_3)**

**USB 2.0 Port (5VSB Only) (USB2\_3)**



**36 : Top : COM Port (COM3) (RS232/422/485)\*  
 Bottom : COM Port (COM4) (RS232/422/485)\***

\* This motherboard supports RS232/422/485 on COM3, 4 ports. Please refer to below table for the pin definition. In addition, COM3, 4 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

**COM3, 4 Port Pin Definition**

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	RX+	N/A
3	TXD, Transmit Data	TX+	RTX+
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	N/A	N/A	N/A

**37 : ATX/AT Mode Jumper (SIO\_AT1)**

Open : ATX Mode

Short : AT Mode

**38 : Top : DisplayPort (DP0)**

Bottom : DisplayPort (DP1)

**39 : DC Jack (DC\_JACK1)**